## PATENT APPLICATION



re the Application of

Batch/Confirmation No.: 5674

Kazuhiko OKAWA et al.

Allowed:

July 26, 2004

Group Art Unit: 2826

Application No.: 09/866,800

Examiner:

J. P. Mondt

Filed:

May 30, 2001

Docket No.: 109657

For:

SEMICONDUCTOR DEVICE HAVING ELECTROSTATIC PROTECTION

CIRCUIT AND METHOD OF FABRICATING THE SAME

## LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please substitute the attached 5 sheets of formal drawings depicting Figures 1, 22-24, 28 and 29 for the corresponding informal drawings filed May 11, 2004. The informal drawings were indicated as being acceptable in the Notice of Allowance mailed July 26, 2004.

Respectfully submitted,

Registration No. 27,075

Jaquelin K. Spong Registration No. 52,241

JAO:JKS/tbh

Date: September 30, 2004

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DEPOSIT ACCOUNT USE **AUTHORIZATION** Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461